Prof. Sergio Saponara

Subjects: Transportation | Engineering, Electrical & Electronic
Contributor: Sergio Saponara

Sergio Saponara is an Italian scientist, engineer and entrepreneur, active in the fields of electronics and automotive engineer.

Automotive Electronics   Powertrain Electrification   Digital Electronics   industry4.0

embedded HPC systems   HW and embedded security

Sergio Saponara is an Italian scientist, engineer and entrepreneur, active in the fields of electronics and automotive engineer. He is Full Professor of Electronics at the University of Pisa, Italy, where he is also President of the BSc and MSc degrees in Electronic Engineering. He got his Master and PhD degrees cum Laude in Electronic Engineering in 1999 and 2003, respectively. He started his research activities in the fields of digital electronics in 1999 first with a research grant from the National Research Council and then a grant from STMicroelectronics. In 2002 he has been a Marie Curie Research Fellow at IMEC, Belgium, Leuven. Since 2005 he is the teaching responsible of the courses in Automotive Electronics, and Electronic Systems for Robotics and Automation at University of Pisa, where he contributed to the foundation of the MSc degree in Cybersecurity, being responsible of HW and Embedded Security. Since 1999 he is also teaching Electronics at Italian Naval Academy. As an entrepreneur, he has co-founded in 2014 the company IngeniArs, winner of several innovation prizes, such as the H2020 SME Instrument. Since 2016 he is founder and director of the Summer School in Enabling Technologies for Industrial IoT, awarded in 2017 and 2018 by the IEEE CAS society. Since 2020 he is director of the Automotive Electronics and Powertrain Electrifications specialization course retraining in the field of vehicle electrification 100 R&D of the Vitesco Technologies (Continental) group.

He co-organized more than 150 conferences and he is Associate Editor of the journals Energies, Sensors, Designs, IEE Electronic Letters, SpringerNature Journal of Real Time Image Processing, IEEE Canadian Journal of Electrical and Computer Engineering, IEEE Consumer Electronics Magazine, IEEE Vehicular Technology Magazine.

Currently, he coordinates the University of Pisa in the H2020 European Processor Initiative where is WP leader for security HW implementation and member elected of the steering committee. He has also on-going projects with MIT on power converters and e-drives for electric/hybrid vehicles. He has co-authored about 300 scientific articles, 4 books, 10 journal special issues, and 20 patents. In his technology transfer activity he has collaborated with Magneti Marelli, Maserati, STMicroelectronics, Renesas, Intel, Huawei, Infineon, Ericsson, P2P, Solari, Trenitalia, AMS, RiCo, INTECS, IDS, Sitael, Leonardo. He has worked as evaluator for scientific projects for the governments

of Italy, Romania, Kazakistan, and the European Commission. He has been selected as an expert for the AI4people initiative of the European Institute for Science Media and Democracy.

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