Key Technologies for Multi-Parallel Inverter Microgrid Systems

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As an important form of distributed renewable energy utilization and consumption, the multi-parallel inverter microgrid system works in both an isolated and grid-connected operation mode. Secondary-frequency and voltage-regulation control are very important in solving problems that appears in these systems, such as the distributed secondary-frequency regulation real-time scheme, voltage and reactive power balancing, and the secondary-frequency regulation control under the disturbances and unbalanced conditions of a microgrid system.

Keywords: multi-parallel inverter microgrid ; voltage ; virtual impedance technique

1. Introduction

A number of general analyses and solution techniques have been developed for the current problem of secondary voltage frequency regulation in multi-parallel inverter microgrids. In this section, some of these key technologies are introduced and analyzed in terms of their implementation methods and principles. Development trends of the methods and technologies are also analyzed.

2. Virtual Impedance Technique

Virtual impedance is a classical method used to improve the stability and reactive power-balancing accuracy of multiple parallel inverter microgrid systems ^[1]. A schematic diagram of the virtual impedance method is shown in **Figure 1**.

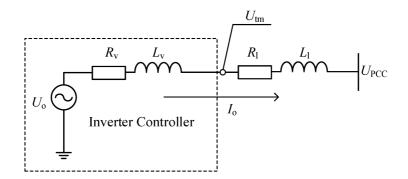


Figure 1. Schematic diagram of the virtual impedance technique.

 R_v and L_v are the resistive and inductive parts of the inverter virtual impedance. U_{tm} is the output port voltage, and I_0 is the inverter output current. The principle of the virtual impedance method is that the output port voltage of the inverter is changed to U_{tm} by virtual impedance loop calculations, as if an equivalent impedance of R_v and L_v is connected in series between the inverter output port and the original output port behind the droop-control loop.

The dq-axis virtual impedance expressions are as follows:

$$u_{ ext{tm},dq} = u_{ ext{o},dq} - egin{pmatrix} R_v + L_v rac{ ext{d}}{ ext{d}t} & -\omega L_v \ \omega L_v & R_v + L_v rac{ ext{d}}{ ext{d}t} \end{pmatrix} i_{ ext{o},dq}$$
(1)

Since the inverter control inner loop is the decoupled dq-axis PI controller, Equation (1) is commonly used for the implementation of the virtual impedance method. Thus, based on the inner current-voltage control loop illustrated, the

virtual impedance control technique can be achieved by changing the reference inputs of the voltage loop from the original $u_{c,d}^*$ and 0 to $u_{tm,dq}$.

The key problem with the implementation of Equation (1) is that the differential signal for the output current in it is difficult to realize. Differential calculations will lead to a significant amplification of the signal and numerical noise and should generally be avoided in inverter control loops. There are two main ways to handle the differential calculation issue. One solution is to approximate the differential link as a high-pass filter. Then, the control method in Equation (1) becomes:

$$u_{ ext{tm},dq} = u_{ ext{o},dq} - egin{pmatrix} R_v + L_v rac{S}{T_{ ext{hpf}}S+1} & -\omega L_v \ \omega L_v & R_v + L_v rac{S}{T_{ ext{hpf}}S+1} \end{pmatrix} i_{ ext{o},dq}$$
 (2)

The virtual impedance method is defined as transient virtual impedance in ^[2]. The other solution for differential calculations ignores the dq-axis current differentiation, and the virtual impedance form becomes:

$$u_{ ext{tm},dq} = u_{ ext{o},dq} - egin{pmatrix} R_v & -\omega L_v \ \omega L_v & R_v \end{pmatrix} i_{ ext{o},dq}$$
 (3)

A significant effect of virtual impedance control is that it can improve the accuracy of droop inverter reactive power distribution. When the line impedance of the system is proportional to the droop coefficient, that becomes:

$$rac{X_{1,j}}{X_{1,k}} = rac{n_{U,j}}{n_{U,k}}, j,k = 1,2,\ldots n, j
eq k$$
 (4)

Ignoring the voltage drop due to the reactive droop loop, the Q-U droop relationship will be restored. Generally, it is difficult to achieve the condition that the line impedance is proportional to the inverter droop coefficient. However, with the virtual impedance control technique, the relationship in Equation (4) can be restored with the virtual impedance technique. In addition, the resistive part of the virtual impedance can enhance the small-signal stability of the system ^[3]. There has been a considerable amount of research on virtual impedance methods and their applications. Some of these typical studies and their corresponding abstracts are reviewed in **Table 1** ^{[3][4][5][6][7][8][9][10][11][12][13][14][15][16][17][18][19][20][21][22][23]}.

Research Category	Literature	Abstract	Cost	Complexity	Effects
Method Proposed	[4][6]	Adaptive virtual impedance method for reactive power sharing based on transient virtual impedance.	Medium	Low	Power sharing
	[8]	Particle swarm optimization based on virtual impedance design for oscillation damping.	High	Medium	Stability
	[11]	Virtual impedance method with dq current droop and additional compensator.	Medium	Low	Power sharing and stability
	[15]	Proposes a distributed adaptive virtual impedance method for stability enhancement. CAN bus communication is implemented.	High	High	Stability
	[17]	Successive approximation-based virtual impedance method. The value of L_v and R_v is time dynamic.	Medium	Low	Stability
	[<u>19]</u>	Adaptive virtual impedance method using consensus algorithm.	High	High	Power sharing
	[22]	DQ frame asymmetrical virtual impedance method.	Medium	Medium	Harmonic wav sharing
	[23]	Online optimization of virtual impedance scheme.	High	Medium	Power sharing

Table 1. Review list of research on virt	tual impedance technique.

Research Category	Literature	Abstract	Cost	Complexity	Effects
Novel Application	[5]	Uses source-side virtual impedance method for cascaded system stability enhancement.	Medium	High	Stability
	[9]	Uses virtual impedance method for capacitive coupling inverters.	Low	Low	Stability on special situation
	[10]	Uses virtual impedance for active and reactive power decoupling.	Medium	High	Power sharing
	[18]	Multi-frequency band virtual impedance for better current quality.	High	High	Harmonic wave sharing
	[20]	Virtual impedance method for accurate harmonic power sharing.	High	High	Harmonic power sharing
Analysis	[3]	Virtual impedance technique stability and function of circling current suppression.	١	١	١
	[7][<u>16]</u>	Parameter design and analysis of virtual impedance method for week grids.	١	١	١
	[12]	Critical clearing time analysis for inverter-based power system under the impact of virtual impedance.	١	١	١
	[<u>13][21]</u>	Virtual impedance method and the harmonic stability and unbalanced operation condition analysis.	١	١	١
	[14]	Rate of change of frequency analysis for inverters with virtual impedance control scheme.	١	١	١

3. Consensus Algorithm and Event-Triggered Mechanism

The consensus algorithm is derived from graph theory. Consider a fully connected undirected graph Ξ containing *M* nodes inside. *G* is the set of all edges formed in the graph Ξ . Y_{ij} denotes the edges connecting node *i* and node *j*.

A is defined as the $M \times M$ adjacency matrix with the elements a_{ij} defined as follows:

$$a_{ij} = \begin{cases} 1, i \neq j, and \ Y_{ij} \in G \\ 0, others \end{cases}$$
(5)

Then, define \boldsymbol{D} as the connectivity matrix whose elements are defined as follows:

$$D = diag(d_i)$$

$$d_i = \sum_{j=1}^{M} a_{ij}$$
(6)

Finally, define *L* as the Laplace matrix, and it becomes L = D - A. The matrix *L* is a symmetric matrix that has 0 eigenvalues, and its corresponding eigenvector is an all-one vector. Furthermore, according to Green's disc theorem ^[24], it is known that the matrix *L* is a semi-positive definite matrix, which becomes the reason that the system with the consensus algorithm is stable.

The widely adopted leader-follower type of consensus algorithm is constituted as follows:

$$\dot{x}_{i} = u_{i}$$
 $u_{i} = \sum_{j=1}^{M} a_{ij} (x_{j} - x_{i}) + b_{i} (x^{\text{ref}} - x_{i})$
(7)

In Equation (7), x_i is the state variable, and u_i is the control input. b_i is the pin coefficient. $b_i = 1$ if the *i*-th node is the leader node, and $b_i = 0$ otherwise. It can be proved [24] that the dynamic of (7) eventually converges to the reference value x^{ref} for all states x_i . If the state variables in (7) are the frequency or voltage variables of a multiple parallel inverter system, the frequency and voltage of the inverters in the system will be regulated to the target value.

Recently, the consensus algorithm has been widely applied to solve the secondary-frequency and voltage-regulation problem in microgrids. However, the consensus algorithm will bring immense communication pressure to the inverter. In order to reduce the communication pressure caused by the consensus algorithm, the consensus algorithm with an event-triggering mechanism improvement is gaining more and more attention ^[25].

The event-triggered mechanism is similar to the idea of variable structure control ^[26]. The system control state updates and broadcasts its state when the system reaches a certain threshold away from the stable equilibrium point. The controller remains silent when the system is within the state threshold or the state reaches the convergence phase plane, at which point the system will spontaneously converge to the stable state even if the control state is not updated. **Figure 2** shows the regulation pattern of the system with the event-triggered mechanism.

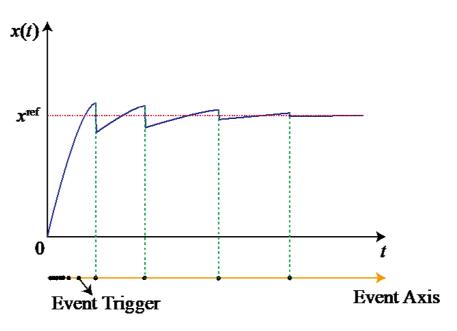


Figure 2. Regulation pattern with event-triggered mechanism.

Research on the application of event-triggered mechanisms to improve the consensus algorithm in order to optimize the frequency and voltage regulation process of multi-parallel inverter microgrids is presented in ^{[27][28][29][30][31][32][33]}. To summarize the existing designs and applications of event-triggered-based consensus algorithms, the following highlights can be mentioned regarding the design of event-triggered mechanisms.

3.1. Event-Trigger Function

The term "event" in the event-trigger mechanism refers to the event that the event function f_j is greater than 0. This means that when the event-trigger function satisfies $f_j(t) \ge 0$, the inverters broadcast their latest output signal to the connected inverters connected on the communication network. Further, when the event-trigger function satisfies $f_j(t) < 0$, the individual inverters remain silent, while the consensus algorithm of each inverter uses the last signal sent by the inverter communicating with it. There is a certain pattern to the design of event-trigger functions for event-triggered mechanisms, which are often in the following form: $f_j(t)=e_j(t)2-M(\theta)z_j(t)2$

 $M(\theta)$ is a specific function consisting of various types of coefficients related to communication or network structure parameters. $e_j(t)$ is the error function consisting of the actual value of the state variable and the consensus state variable, and $z_i(t)$ is the consensus algorithm function. For example, the event-trigger function in $\frac{[27][31]}{2}$ are both in this form.

3.2. Zeno Phenomenon

The Zeno phenomenon refers to infinite triggering times in a short period of time ^[30]. No Zeno phenomenon is an important criterion for the design of event-triggered mechanisms. In general, the method used to prove that no Zeno phenomenon will occur is to show that there is a lower bound for the event-trigger time from the designed method.

3.3. Chattering

Numerical chattering can lead to frequent meaningless event triggers after the consensus algorithm has reached a steady state. Therefore, to further reduce the event-trigger frequency, the absolute or squared value of the error function $e_j(t)$ or the consensus algorithm function $z_j(t)$ can be used as an indicator to set the corresponding threshold for an anti-chattering design. However, the anti-chattering threshold should not be set too large, or it will affect the stability of the event-triggering consensus algorithm.

3.4. Consensus Signal Selection

In [27][28][29][30][31][32][33], the event-triggered consensus algorithm is adopted to design the secondary-frequency and voltage-regulation method, but the consensus signals are selected as the frequency and voltage signals themselves. According to the previous discussion of droop-based control, an important requirement in the design of the secondary-frequency and voltage regulation link based on droop control is that the secondary-frequency and voltage-regulation component of each inverter should be of the same value; otherwise, the droop characteristics will not be maintained. Therefore, in order for the droop characteristics to remain unchanged, the existing event-triggered consensus algorithms for secondary-voltage and frequency regulation adopt additional components to maintain the invariance of the droop relationship among inverters. For example, in ^{[27][30][31]}, the power differential signal is introduced to compensate for the original consensus algorithm to maintain the droop relationship. In ^[32], an additional active power droop consensus component u_{Pi} is added to the consensus algorithm, and the whole method becomes complicated.

At present, the theory on and analysis of consensus algorithms with event-triggered mechanisms are not yet mature. The impact of the event-triggered mechanism on the small signal and transient stability of microgrid systems has not yet been fully studied.

4. Active Disturbance Rejection Control and Robust Control Method

As mentioned before, non-ideal operating conditions such as disturbances, harmonics, and imbalances are common in the operational control of multi-parallel microgrids. Therefore, control methods for frequency and voltage-regulation control under various types of disturbances and uncertainties are widely used. Among them, active disturbance rejection control (ADRC) and robust control are effective control methods for various types of disturbances and uncertainties are widely used.

4.1. Active Disturbance Rejection Control

ADRC is fundamentally different from robust control. ADRC emphasizes the observation of and compensation for these perturbations, and the control design procedure concentrates on the design of the perturbation observer. Robust control emphasizes the minimization of the effect of model uncertainty on the stability of the system by designing a stabilizing controller. Therefore, the design procedure concentrates on optimizing a specific robust stability criterion. In addition, the source of perturbation that is the focus of concentration by ADRC is generally external perturbation, whereas the robust control is generally targeted at model uncertainty.

The key design procedure is to design the extended state observer (ESO). (8) is known as the extended state observer (ESO). β_1 and β_2 are known as the observer gains.

$$\begin{cases} e_1 = z_1 - y \\ \dot{z}_1 = z_2 - \beta_1 e_1 + bu \\ \dot{z}_2 = -\beta_2 e_1 \end{cases}$$
(8)

The advantage of the ADRC control method is its ability to effectively suppress external disturbances, enabling the system to recover to the steady-state equilibrium more quickly after being disturbed. The ADRC control method has been applied in some research to design control methods for microgrid multi-parallel inverter systems with enhanced system immunity [35][36]. Ref. [35] used the ADRC control method to enhance the dynamic immunity of a frequency/voltage secondary

regulation scheme based on a consistency algorithm. In ^[36], the ADRC method is used to enhance the dynamic performance of microgrid inverters.

4.2. Robust Control

The key to design a robust controller is to model the uncertainty of the system. System uncertainty can be divided into additive uncertainty and multiplicative uncertainty. Define z as the uncertainty model output and w as the uncertainty model input. According to the transformation between the state space and the transfer function, the matrix of the transfer function from w to z can be obtained as follows:

$$\begin{pmatrix} \dot{x} \\ z \\ y \end{pmatrix} = \begin{pmatrix} A & B_1 & B_2 \\ C_1 & D_{11} & D_{12} \\ C_2 & D_{21} & D_{22} \end{pmatrix} \begin{pmatrix} x \\ w \\ u \end{pmatrix}, u = Ky H_{zw}(s) = G_{11} + G_{12}K(I - G_{22}K)^{-1}G_{21} G_{ij}(s) = C_i(sI - A)^{-1}B_j + D_{ij}$$
(9)

The H_{∞} control optimality problem can then be formulated as finding the control gain matrix **K** such that the H_{∞} norm of the transfer function $H_{zw}(s)$ is minimized:

$$\min_{K} \left\| \boldsymbol{H}_{zw}(s) \right\|_{\infty} \tag{10}$$

Furthermore, the H_{∞} control suboptimal problem is formulated as finding a control gain matrix K that satisfies the condition that the H_{∞} norm of $H_{zw}(s)$ is smaller than the given constant γ :

$$\left\|\boldsymbol{H}_{zw}(s)\right\|_{\infty} < \gamma \tag{11}$$

The basic principle of H_{∞} control in robust control is presented above. A detailed frequency domain H_{∞} controller design flow is given in ^[37] for a typical photovoltaic DCDC converter. Summarizing the design in ^[37], the design of the frequency domain H_{∞} controller is concluded in the following flow.

(a) Transform the uncertainty terms in the control model into additive uncertainty form or multiplicative uncertainty form.

(b) Extract and separate the uncertainty terms by linear fractional transformation (LFT). Then, select the suitable weight function W and integrate it into the model G, resulting in a nominal model/uncertainty term/control matrix corresponding to the G- Λ -K form.

(c) The H_{∞} optimal or sub-optimal problem is solved by μ synthesis algorithm to obtain the required control gain matrix K.

There have been some studies and applications of H_{∞} control for the problem of the secondary regulation of frequency and voltage in a non-ideal microgrid ^{[38][39][40][41][42]}. In ^[38], the full flow of the design and stability analysis of the H_{∞} controller for grid-connected inverters is presented. The H_{∞} problem is solved by the μ synthesis algorithm. In ^[39], an inner-loop sliding-mode control and an outer-loop hybrid H_2/H_{∞} control was adopted to design a grid-connected inverter with strong robustness, and the linear matrix inequality (LMI) algorithm was used to solve the H_{∞} problem. In ^[40], an H_{∞} controller is applied to a frequency quadratic regulation control loop. The H_{∞} problem is solved by the μ synthesis algorithm. In ^[41], the H_{∞} controller was used to design the control inner loop of a microgrid inverter for islanded operation, and the LMI algorithm was used to solve the H_{∞} problem. In ^[42], H_{∞} controllers are designed for improving the small-signal stability of microgrid inverters against communication delays and non-linear load conditions.

For the application of robust H_{∞} control methods in the field of frequency/voltage secondary regulation of microgrid inverters, the number of applications and the amount of research are still relatively small, and a unified scheme has not yet been developed. More in-depth and standardized work is desired in the future.

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